

PWM Controller

Description

CD5025 is a voltage PWM controller that implements power converters utilizing the Active Clamp / Reset technique. With the active clamp technique, higher efficiencies and greater power densities can be realized compared to conventional catch winding or RDC clamp / reset techniques.

Two control outputs are provided, the main power switch control (OUT_A) and the active clamp switch control (OUT_B). The two internal compound gate drivers parallel both MOS and Bipolar devices, providing superior gate drive characteristics. This controller is designed for high-speed operation including an oscillator frequency range up to 1MHz and total PWM and current sense propagation delays less than 100ns.

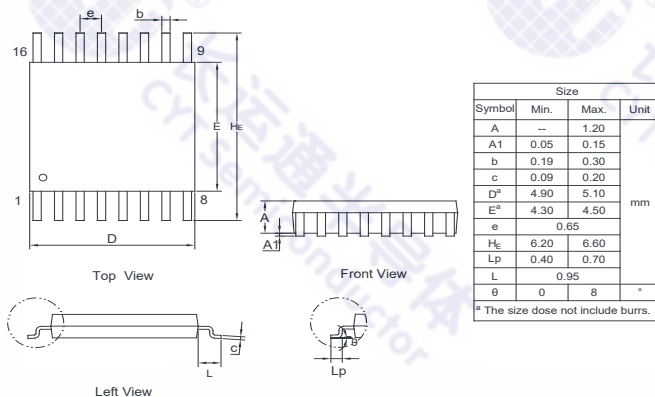
Absolute Maximum Ratings

VIN to GND	-0.3V to 105V
VCC to GND	-0.3V to 16V
CS1, CS2 to GND	-0.3V to 1V
All other inputs to GND	-0.3V to 7V
Junction Temperature	150°C
Storage Temperature Range	-65°C to 150°C

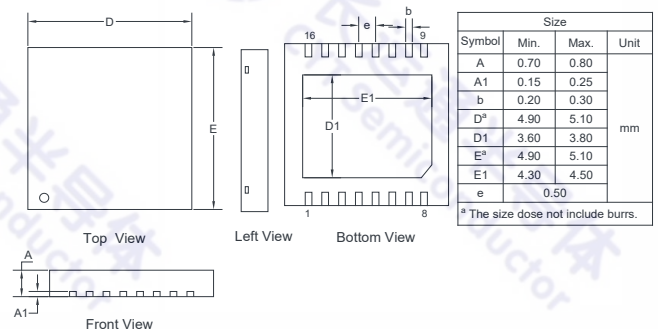
Recommended Operating Conditions

VIN Voltage	13V to 100V
VCC Voltage	8V to 15V
Operating Junction Temperature (TJ)	-40°C to 125°C

Package Diagram



TSSOP16



WSOP16

Typical Application Circuit

